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January 5, 2007

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Nui Chong; Aaron Rogers; Kerry Ilgenstein
Assignee: Lattice Semiconductor Corporation
Title: Electrostatic Discharge Protection Circuits
Serial No.: 10/808,627 Filed: March 24, 2004
Examiner: Dang, Robert Trong Group Art Unit: 2838
Docket No.: M-15338 US Confirmation No. 6086

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter; (1 page)
(2) Response to Office Action (10 pages)



No additional fee is required.



The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <i>After</i> <u>Amendment</u>	Highest No. Previously Paid <u>For</u>	Present Extra	=	0	x \$50.00	\$	Additional Fee
Total Claims	22	Minus	20	=	0	x \$50.00	\$	0
Independent Claims	3	Minus	3	=	0	x \$200.00	\$	0
	<input type="checkbox"/> Fee of _____ for the first filing of one or more multiple dependent claims per application						\$	

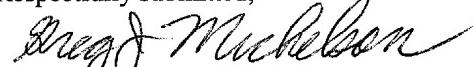
Total additional fee for this Amendment:

- Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- Please charge our Deposit Account No. 50-2257 in the amount of \$ 0
- Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 50-2257

Total: \$ 0

ELECTRONICALLY FILED
JANUARY 5, 2007

Respectfully submitted,



Greg J. Michelson
Attorney for Applicants
Reg. No. 44,940